



## Electrical Instrumentation Laboratory

The University of Toledo  
Electrical Engineering Technology program, College of Engineering  
EET-2420-001, CRN: 41165

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|--------------------------|---------------------------------------|------------------------|--|
| <b>Instructor:</b>       | William Mugge                         | <b>Class Location:</b> | N/A  |
| <b>Email:</b>            | William.Mugge@utoledo.edu             | <b>Class Day/Time:</b> | N/A  |
| <b>Office Hours:</b>     | (Insert Availability/by appointment)* | <b>Lab Location:</b>   | NE2380   |
| <b>Office Location:</b>  | NE 1639                               | <b>Lab Day/Time:</b>   | M 8:00 – 9:30 am, M 9:35 – 11:05 am,<br>and W 5:25 – 6:54 pm |
| <b>Instructor Phone:</b> | 419-530-3277                          | <b>Credit Hours:</b>   | 1.0  |
| <b>Offered:</b>          | Fall 2019                             |                        |  |
| <b>Course Website:</b>   | Blackboard                            |                        |  |

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### CATALOG/COURSE DESCRIPTION

Provides an opportunity for Computer Science and Engineering Technology students to gain laboratory experience with basic electrical instrumentation and basic computer components.

### COURSE OVERVIEW

Provide hands-on lab sessions on order to:

To introduce the following electrical concepts:

- Series, parallel, and series - parallel circuits
- Ohms Law, Kirchhoff's Law
- Bench-top equipment such as power supplies and signal generators
- Measuring devices such as Digital Multi Meter and oscilloscopes
- Electrical drawing to circuit construction exercises
- Resistors, capacitors, and diodes
- Logic gates

### STUDENT LEARNING OUTCOMES

ABET Program

Student Learning Outcomes:

- a) an ability to select and apply the knowledge, techniques, skills, and modern tools of the discipline to broadly defined engineering technology activities;
- c) an ability to conduct standard test and measurements; to conduct, analyze and interpret experiments; and apply experimental results to improve processes;

Electrical Engineering Technology Outcomes:

- a) the application of circuit analysis and design, computer programming, associated software, analog and digital electronics, and microcomputers, and engineering standards to the building, testing, operation, and maintenance of electrical/electronic(s) systems.



## TEACHING METHODOLOGY

This is an active learning course that will require students to be fully engaged. Face-to-face instructions in the laboratory environment will provide maximum help to students. Lab reports with review questions included and a comprehensive test will be given. The purpose of this course is to help students understand the fundamental control theories and use those theories to solve the relevant problems. Students are welcome to ask questions and discuss problems. The course will be instructed based on the philosophy of cycling education:

- through the theory to understand the formula
- through the usage of formulae to the understand examples
- through the examples to solve similar exercise problems
- through face-to-face instruction to improve learning efficiency
- through lab guides reinforcement of principles provided in lecture
- through exams to emphasize importance and clarify confusion
- though taking this course to have the capability to self-study for future work or research

Recommendations for success:

- Come to lab sessions and take notes
- Review the relevant lab report contents and lecture materials before each test or exam

Never hesitate to ask for help from the instructor

## PREREQUISITES AND COREQUISITES

None

## TEXTS AND ANCILLARY MATERIALS

None

## TECHNOLOGY REQUIREMENTS

Web assist - Blackboard <http://blackboard.utdl.edu/>

## ACADEMIC POLICIES

*Students in this course should be familiar with policies that govern the institution's academic process.*

*Please find a total list of undergraduate Academic Polices:*

<http://www.utoledo.edu/policies/academic/undergraduate/>

**Missed Class Policy:** Students are expected to attend each class session. Should there be an unexpected absence on your part, you must notify me **by e-mail**. Please read the Missed Class Policy:

<http://www.utoledo.edu/policies/academic/undergraduate/>

**Academic Dishonesty Policy:** MISCONDUCT: Students may work together on homework problems but must submit their own work. Students are not allowed to work together on exams. Any occurrence of academic misconduct will follow the policy for Academic Dishonesty. Students that receive a reduced course grade as a result of academic misconduct will not be allowed to withdraw from the course and may not petition for a GPA recalculation after retaking the course. Please refer to the [Academic Dishonesty](#) and [Academic Grievance](#) policies for more details. If you are caught in the act of plagiarism or



cheating, you will be reported to the Dean and you will be placing your entire academic career at risk. There are no reasonable excuses or exceptions for cheating and plagiarism. Examples include but are not limited to as followings. Please read the <http://www.utoledo.edu/policies/academic/undergraduate/>

1. Plagiarizing or representing the words, ideas or information of another person as one's own and not offering proper documentation;
2. Giving or receiving, prior to an examination, any unauthorized information concerning the content of that examination;
3. Referring to or displaying any unauthorized materials inside or outside of the examination room during an examination;
4. Communicating during an examination in any manner with any unauthorized person concerning the examination or any part of it;
5. Giving or receiving substantive aid during an examination;
6. Commencing an examination before the stipulated time or continuing to work on an examination after the announced conclusion of the examination period;
7. Taking, converting, concealing, defacing, damaging or destroying any property related to the preparation or completion of assignments, research or examination;
8. Submitting the same written work to fulfill the requirements for more than one course.

**Exams Policy:** There will be no makeup tests given. It is the student's responsibility to contact the instructor prior to the scheduled exam if an absence cannot be avoided in order to make alternate arrangements.

**Attendance Policy:** Attendance will not be taken, but students are fully responsible for being present at all examinations, and for all materials, announcements, or changes in the schedule in class. In case of excused absence, **any missed work must be done** and written documentation of the circumstance (such as a doctor's signed note) must be provided to be kept on file.

## **COURSE EXPECTATIONS**

**Quizzes:** Weekly reviews are included on the assigned lab guides.

**Laboratory reports:** Lab reports are typically due at the end of class, and not later the class session following the week of performance.

**Lab reports and exams:** The final answer alone is not enough to get credit. Solution steps must be shown to get credit.

**Electronica Policy:** No electronic items: cellular telephones, Blackberrys, personal digital assistants, digital music players or similar items that may disrupt the learning environment may be used at any time for any purpose during the classroom or laboratory time. If a cell phone must be kept on due to a potential emergency situation, it must be on a silent setting. If an emergency call must be taken during a class, the student must leave the classroom prior to answering the call and not return until the call is completed. See also Article IV.B Conduct Rules and Regulations of the Student Code of Conduct at the University of Toledo, which states, in part:

“Disruption of operations of the University Community. Disruption is an action or combination of actions by an individual or a group, which unreasonably interferes with, hinders, obstructs, or prevents the right of others to freely participate in its programs, services, or academic settings. This



may include but is not limited to a disruption by the use of pagers, cell phones and/or any other communication devices.”

If there is a conflict or misunderstanding, please see me privately to work out a resolution.

### OVERVIEW OF COURSE GRADE ASSIGNMENT

Assigned work turned in passed the due date with no prior agreement with the instructor may be accepted, accepted with point reduction, or not at all.

The final exam and course final grade use the same formula, scale, and weights.

Midterm grades will be presented per university requirements and based on the current updated cumulative scores obtained by the students usually the first 5 or 6 weeks.

#### Final Grading

|                         |                 |                 |
|-------------------------|-----------------|-----------------|
| Quizzes: 20%            | A: 93.00-100    | C: 73.00-76.99  |
| Lab reports: 20%        | A-: 90.00-92.99 | C-: 70.00-72.99 |
| Mid-term test: 20% (T1) | B+: 87.00-89.99 | D+: 67.00-69.99 |
| Second test 20% (T2)    | B: 83.00-86.99  | D: 63.00-66.99  |
| Final Exam: 20%         | B-: 80.00-82.99 | D-: 60.00-62.99 |
| Total: 100%             | C+: 77.00-79.99 | F: < 59.99      |

### UNIVERSITY POLICIES\*

#### Policy Statement on Non-Discrimination on the Basis of Disability (ADA)

The University is an equal opportunity educational institution. Please read [The University's Policy Statement on Nondiscrimination on the Basis of Disability Americans with Disability Act Compliance](#).

Students can find this policy along with other university policies listed by audience on the [University Policy webpage](http://www.utoledo.edu/policies/audience.html/#students) (<http://www.utoledo.edu/policies/audience.html/#students>).

#### Academic Accommodations\*

The University of Toledo embraces the inclusion of students with disabilities. We are committed to ensuring equal opportunity and seamless access for full participation in all courses. For students who have an accommodations memo from Student Disability Services, I invite you to correspond with me as soon as possible so that we can communicate confidentially about implementing accommodations in this course. For students who have not established affiliation with Student Disability Services and are experiencing disability access barriers or are interested in a referral to healthcare resources for a potential disability or would like information regarding eligibility for academic accommodations, please contact the [Student Disability Services Office](http://www.utoledo.edu/offices/student-disability-services/) (<http://www.utoledo.edu/offices/student-disability-services/>) by calling 419.530.4981 or sending an email to [StudentDisability@utoledo.edu](mailto:StudentDisability@utoledo.edu).

### ACADEMIC AND SUPPORT SERVICES

Please follow this link to view a comprehensive list of [Student Academic and Support Services](http://www.utoledo.edu/studentaffairs/departments.html) (<http://www.utoledo.edu/studentaffairs/departments.html>) available to you as a student.

In addition to visiting the instructor, which is highly encouraged, several offers additional support, are available which could aid you in succeeding in this course:



- Engineering Technology Department Teaching Assistants - NE 1604 & NE 1606

## SAFETY AND HEALTH SERVICES FOR UT STUDENTS

Please use the following link to view a comprehensive list [Campus Health and Safety Services](#) available to you as a student.

## COURSE SCHEDULE

**No Class Dates:** Per university calendar

**Final Exam Date:** Per university schedule

**Course Schedule** (Subject to Change depending on the course progress)

| WEEK | DATES | TOPIC                          | LEARNING OUTCOME(S) | ASSIGNMENTS DUE           |
|------|-------|--------------------------------|---------------------|---------------------------|
| 1    | 8/26  | Voltage and Current            | All                 | Activity sheet 1          |
| 2    | 9/9   | Resistors                      | All                 | Activity sheet 2          |
| 3    | 9/16  | Series circuit                 | All                 | Activity sheet 3          |
| 4    | 9/23  | Parallel circuits              | All                 | Activity sheet 4          |
| 5    | 9/30  | Series and parallel circuits 1 | All                 | Activity sheet 5          |
| 6    | 10/7  | Series and parallel circuits 2 | All                 | Activity sheet 6          |
| 7    | 10/14 | Oscilloscope                   | All                 | Activity sheet 7          |
| 8    | 10/21 | RC circuit                     | All                 | Activity sheet 8          |
| 9    | 10/28 | Diodes                         | All                 | Activity sheet 9          |
| 10   | 11/4  | NOT gate IC                    | All                 | Activity sheet 10         |
| 11   | 11/18 | AND gate IC                    | All                 | Activity sheet 11         |
| 12   | 11/25 | OR and NAND gate ICs           | All                 | Activity sheets 12 and 13 |
| 13   | 12/2  | Ex or and NOR gate ICs         | All                 | Activity sheets 14 and 15 |
| 14   | 12/9  | Final Exam                     | All                 |                           |
|      |       |                                |                     |                           |

## NOTES:

- 1) Remember to delete all directions/notations highlighted in yellow (all in brackets) before finalizing your syllabus for the semester.
- 2) Remove and/or Update the footer from Syllabus Template to course information and college/department, you can also delete the word *\*Required*.